

Class	Subclass
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PATENT NUMBER

**U.S. UTILITY Patent Application**

**O.J.P.E.**

**PATENT DATE**

SCANNED

## Q.A.

APPLICATION NO. 09/712246

CONT/PRIOR  
F

**CLASS**

SUBCLASS  
744

ART UNIT  
~~9919~~

**EXAMINER**

## APPLICANTS

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## titles

Semiconductor memory device with built-in self test circuit  
operating at high rate

PTO-2040  
12/99

# ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
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